

Monday 24 February

AC SEMINAR

at 11.00 hrs – AT Auditorium
bldg 30, 7th floor

Description and Status Report of the Supercollider

by John L. WARREN / SSCL – Dallas, Texas

*General description of the SSC Laboratory, planning for the
collider rings and the injector complex. Photographs of the
construction.*

Wednesday 26 February

PRESENTATION

at 10.00 hrs – CN Auditorium
bldg 31/3-005

Use of Oracle at British Airways

by Neil Sidwell

Information : G. Moorhead

TRAINING AND EDUCATION ENSEIGNEMENT

ENSEIGNEMENT GENERAL Tél. 5811

Jeudi 20 février

à 13.00 h – Amphithéâtre

Science pour tous

par Rafel CARRERAS

ACADEMIC TRAINING Tel. 2844

Lecture series for postgraduate students

17, 19 & 20 February

10.45 to 12.00 hrs – Auditorium

18 February

10.45 to 12.00 hrs – Council Chamber

Introduction to accelerators

17.2. Linear accelerators (by guest speaker M.
Weiss)

18.2. RF bunches and buckets

19.2. Electrons

20.2. Instabilities

by E.J.N WILSON / CERN-PS



ENSEIGNEMENT TECHNIQUE TECHNICAL TRAINING

☎ 44.60

eductech@cernvm

Technical seminar

Friday March 13 - 9 h.00-12 h.00

BOUNDARY-SCAN TEST (BST)

by H. BLEEKER, Philips BST Group

The present way of testing Printed Circuit Boards (PCBs) by means of bed-of-nails fixtures has reached its limit of feasibility. This is caused by the miniaturization of PCBs and the application of complex ICs such as ASICs.

Boundary-Scan Test technology is a new approach to PCB testing. It offers access to the internals of electronic circuits by means of a test bus which may consist of merely four wires. The implementation of the new test technology requires an extra investment in chip design. BST is therefore more an integral management approach rather than the introduction of just a new design technology.

The presentation will address the following points :
Technology changes - principle of the standard - case study - impact on product life cycle - impact on product quality and reliability - product overview (test preparation and test equipment).

As the number of participants is limited, those who would like to follow this seminar should register themselves by e-mail (eductech@cernvm) or by using the coupon below.

No fee will be charged to the division.

To : Technical Training Secretariat

I would like to participate in the **Boundary-Scan Test** seminar

Name & First name :

Div : Date :